DECLARATION

As below named inventors, we hereby declare that:

Our residences, post office addresses and citizenships are as stated below next to our names; that

We verily believe we are joint inventors of the invention entitled:

METHOD FOR FORMING A DIELECTRIC LAYER AND SEMICONDUCTOR DEVICE INCORPORATING THE SAME, (Docket No. MIO 0060 PA), described and claimed

	in the attached specification;
X	in the specification filed August 31, 2000, as U.S. Application Serial No.
	<u>09/653,096</u> , and as amended

We hereby authorize the attorney(s) and/or agent(s) appointed herein to indicate above whether the invention is described and claimed in an attached specification and to provide the Filing Date and Serial No. of the corresponding U.S. Application, if previously filed.

We hereby state that we have reviewed and understand the contents of the above identified specification, including the claims as filed and as amended by any amendment referred to above.

We acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56(a).

We further declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full name of first Inventor: DON CARL POWELL					
Inventor's signature and fowl					
Date: 11/01/00					
Residence:		astgate Drive no 83716-5638			
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Post Office Add	dress: C/O	Micron Technology, Inc. 8000 S. Federal Way Boise, ID 83706-9632			
Full name of second Inventor: GARRY ANTHONY MERCALDI					
Inventor's signature					
Date: 11/01/00					
Residence:		h Pine Bar Place Idaho 83742			
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POWER OF ATTORNEY

Applicant: <u>Don</u>	Carl Powell and	Garry Anthony N	vlercaldi	
			August 31, 2000	
				SEMICONDUCTOR
DEVICE INCOR			1110 231 1 231 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	BEHILOTABOUTOR
		<u> </u>		
	CERT	IFICATE UNDE	R 37 CFR 3.73(b)	
at 8000 S. Federa	l Way, Boise, ID	83706-9632 cert		e, with a place of business signee of the entire right, either:
A. [X] An assignate which is at		ventor(s) of the p	atent application ide	entified above, a copy of
OR				
current assignee	as shown below:			entified above, to the
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Reel	_ Frame, or	r for which a cop	y thereof is attached	a.
2 From:		To		
			nd Trademark Offic	e at
			y thereof is attached	
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3. From:		To:		
The docu	ment was recorde	ed in the Patent a	nd Trademark Offic	e at
Reel	_ Frame, oi	r for which a cop	y thereof is attached	d.
[] Addition	onal documents in	n the chain of titl	e are listed on a sup	oplemental sheet.
[] cobics or assi-	similarity of other	accaments in an	thain of time are a	nached.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the assignee.

Micron Technology, Inc. hereby appoints the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Richard A. Killworth	Reg. No. 26,397
James F. Gottman	Reg. No. 27,262
Timothy W. Hagan	Reg. No. 29,001
James E. Beyer	Reg. No. 39,564
Susan M. Luna	Reg. No. 38,769
Patricia L. Prior	Reg. No. 33,758
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Micron Technology, Inc. hereby declares that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: _	11/10/00		
Name:	Michael	L Lynch	
Title: _			
Signatu	re: <i>t</i>		